



(19) Europäisches Patentamt
European Patent Office
Office européen des brevets



(11) EP 0 963 051 A2

(12)

EUROPEAN PATENT APPLICATION

(43) Date of publication:
08.12.1999 Bulletin 1999/49

(51) Int. Cl.⁶: H04B 1/10, H04B 15/00

(21) Application number: 99201570.1

(22) Date of filing: 19.05.1999

(84) Designated Contracting States:
AT BE CH CY DE DK ES FI FR GB GR IE IT LI LU
MC NL PT SE
Designated Extension States:
AL LT LV MK RO SI

(30) Priority: 02.06.1998 US 89204

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(54) Wireless communication system operating in 2.4 to 2.5 GHz band

(57) A system of digital communication operating in the 2.4 to 2.5 GHz band in the presence of noise from a microwave oven detects quiescent periods.

wave noise and communicates during the quiescent periods.

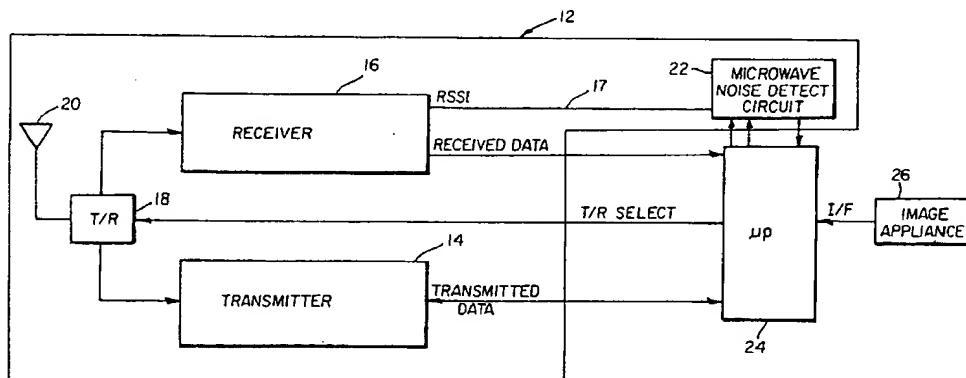


FIG. 2

Description**FIELD OF THE INVENTION**

[0001] This invention relates to a digital wireless communication system operating in the 2.4 to 2.5 GHz ISM (Industrial Scientific and Medical) Band. Such a system is useful, for example, for communicating digital imaging data at high data rates; e.g. rates equal and greater than 10 Mega bits per second (Mbps).

BACKGROUND OF THE INVENTION

[0002] With the advent of digital photography, the wireless transmission of digital images, for example between a transmitting device like a digital camera and a receiving device such as a personal computer or other image appliance such as a printer, has become a desirable feature. High data rate transmissions are desirable because digital images represent a large amount of data and short transmission times are needed. Short transmission times result in shorter wait times while an image is being transferred from a camera to a receiver and in reduced battery power consumption.

[0003] An attractive communication band for such a digital communication system is the 2.4 to 2.5 GHz ISM Band since the band is unlicensed and available internationally. However, a major problem exists with the use of this band for wireless communication. The band is allocated for microwave ovens, which as described below, generate a great deal of noise in the band.

[0004] Radio frequency (RF) transmissions in the 2.4 to 2.5 GHz ISM Band have historically had to deal with the presence of man-made noise from microwave ovens that predominately operate in the center of this band at 2.45 GHz. The noise emanates from the oven by leakage through the enclosure of the oven. The leakage noise has a radiated output power approximately 20 dB greater in strength than that allowed by the FCC for operation of Part 15 non spread spectrum radios (i.e. 10 milliWatts). With over 200 million microwave ovens in use throughout the world, they are by far the greatest and most significant source of noise in this band. Some examples of locations where it would be desirable to transmit images in the presence of microwave oven noise are in the home (particularly in the kitchen), or in a supermarket or department store where a photo kiosk may be located near a microwave oven.

[0005] One possible way to avoid the microwave noise in the 2.4 to 2.5 GHz ISM Band is to use a spread spectrum technique to spread the signal over a band which is much larger than the bandwidth of the signal so that the narrow band noise from the microwave ovens has a reduced effect on the demodulated signal to noise ratio. This technique however is relatively expensive to implement, significantly limits the data throughput (e.g. by a factor of 8 or more) and does not work well if the receiver is very close to the microwave oven.

[0006] There is a need therefore for an improved means of digital communication in the 2.4 to 2.5 GHz ISM band.

5 SUMMARY OF THE INVENTION

[0007] The problem is solved according to the present invention by providing a system of digital communication operating in the 2.4 to 2.5 GHz ISM Band in the presence of noise from a microwave oven that detects quiescent periods in the microwave noise and communicates during the quiescent periods.

15 ADVANTAGES OF THE INVENTION

[0008] The advantages of the present invention are:

1. The RF link does not have to change frequency or use any correlation techniques in order to mitigate the microwave oven noise.
2. The software and clock synchronization circuitry required to execute the off duty cycle transfer has significant cost savings and smaller size requirements than the expensive and bulky circuitry currently used for the prior art spread spectrum techniques described above.
3. During the presence of 50% duty cycle microwave oven noise, image data transfer can be maintained at an average rate of approximately 10 Mbps in as many as four 20 MHz channels simultaneously using simple FSK (Frequency Shift Key) modulation.
4. Using QPSK (Quadrature Phase Shift Key) modulation, 20 Mbps can be maintained as an average rate in each of the 4 separate 20 MHz channels supported by this band. Four channels are supported in many countries, and particularly in the USA. Four systems, each using one channel, can all use the same method simultaneously.

30 40 50 55 BRIEF DESCRIPTION OF THE DRAWINGS

[0009]

- Fig. 1 is a plot of RF noise produced by a microwave oven;
- Fig. 2 is a block diagram of a transceiver according to the present invention;
- Fig. 3 is a block diagram of the microwave oven noise detection circuit; and
- Fig. 4 is a plot of the sync clock signal generated by the microwave oven noise detection circuit.

DETAILED DESCRIPTION OF THE INVENTION

[0010] In addressing the above problem, it is understood that the noise produced by a microwave oven is periodic at a frequency based on the AC power line volt-

age frequency. Referring to Fig. 1, the noise energy waveform 10 produced by a microwave oven is shown. The source of the RF noise energy is the magnetron within the microwave oven, which is driven by the AC power line. The output noise spectrum is pulsed at approximately a 50% duty cycle synchronized to the AC line voltage. The frequency of the microwave noise energy in North American is 60 Hertz. According to the present invention, this pulsed noise is detected and communication is conducted during the quiescent periods, that is during the periods 11 when the microwave oven is off.

[0011] Referring to Fig. 2, a system for communicating digital data according to the present invention includes a transceiver 12 for communicating in the presence of microwave oven. The transceiver includes a transmitter 14, a receiver 16, and a transmit/receive switch 18 that selectively connects an antenna 20 to the transmitter or the receiver. The transmitter 14 is a standard design 2.4 to 2.5 GHz transmitter employing any known modulation scheme such as FSK or QPSK. The receiver 16 is a standard design 2.4 to 2.5 GHz receiver with the capability of demodulating a 20 Mbps FSK or a 40 Mbps QPSK signal. The demodulator in the receiver 16 is for example a Motorola MC13155 integrated circuit. The transceiver 12 also includes a microwave noise detect circuit 22 connected to the RSSI 17 (Receive Signal Strength Indicator) of the receiver 16.

[0012] The transceiver 12 is controlled by a microprocessor 24. The microprocessor 24 may for example be a microprocessor that is also used to control an image appliance 26 (i.e. camera, printer, kiosk, or personal computer), or may be a microprocessor that is specifically supplied with the communication system. The microprocessor 24 controls the transmit/receive switch 18, prepares the data from the image appliance 26 to be transmitted by transmitter 14, and receives the data from the receiver 16 to supply received data to the image appliance 26.

[0013] Referring to Fig. 3, microwave noise detect circuit 22 uses the RSSI signal 17 from the receiver 16 to generate a sync clock 30 and a Lock detect 28 signal as described below, which are supplied to the microprocessor 24 for controlling the communication of data during quiescent periods in the microwave oven noise 10. The main function of this circuitry 22 is to lock on to the envelope of the microwave oven noise 10 using the AM detector in the RSSI circuitry (not shown) to generate a Lock detect signal 28 and a synchronization (Sync) clock 30 for the microprocessor.

[0014] The Lock detect signal 28 informs the microprocessor 24 that a 60 Hertz microwave oven noise signature is present. To generate the Lock detect signal 28, the RSSI 17 input signal coming from the receiver is first buffered 46 and provided both to a phase locked loop circuit 56 and sync clock generator circuitry 32. The phase locked loop circuit 56 consists of a Voltage Controlled Oscillator (VCO) 52, a Phase Comparator 48 that

produces a phase error output signal 54 and a Low Pass loop Filter (LPF) 50. The circuit is designed to provide a Lock detect signal 28 if it is supplied with a 50 to 120 Hertz RSSI signal 17. The circuit is commercially available in integrated circuit form from manufacturers such as Motorola. A commonly used part number is CD4046.

[0015] Fig. 4 shows the Sync clock signal 30 referenced to the microwave oven noise energy in Fig. 1. Sync clock signal 30 is generated separately from Lock detect signal 28 using sync clock generator circuitry 32. Sync clock signal 30 is identical to the RSSI 17 signal in its pulse width, frequency and duty cycle. It is used to predict when the microwave oven will be in the quiescent state. This detect and latch process is necessary since the RSSI 17 signal will not be able to separate the microwave oven noise from the transceiver activity, once data communications commences. Binary counters 34, 36 sample the RSSI 17 high and low periods and load the result into pre-setable counters 38, 40. Once the data is loaded, the Sync clock 30 is generated by monostable multivibrator 42 without the microwave signature provided by the RSSI signal 17. The Sync clock 30 is provided to the microprocessor 24 and is used to enable the transmitting of the image data only during the predicted quiescent or "off" periods 11 in the microwave oven's noise signature. The Sync clock 30 continues to run until the microprocessor determines it is no longer needed, or that it has shifted in phase. The microprocessor then signals the Sync clock to stop using the Sync stop control signal 44.

[0016] Phase shifting can be caused by noise from multiple microwave ovens in different power phases. If three ovens are on simultaneously using three different AC phases, the RSSI signal will be high the entire AC cycle and there will be no Lock detect signal 28. The microprocessor 24 will cause the transceiver to search for a quiet channel. Once a lock is established however, the microprocessor 24 knows when to stop using the Sync clock either when the receiver at the far end signals the transmitter that the microwave oven noise has stopped and it has verified the noise has stopped by listening, or its Lock detect 28 goes high again synchronous with the Sync clock signal 30, indicating that the oven noise is gone, and the transmitter is causing its own lock detect signal.

[0017] This approach works for different AC line frequencies, and works even if there are multiple ovens, or ovens on at least two phases of an AC distribution system.

[0018] The invention has been described in detail with particular reference to certain preferred embodiments thereof, but it will be understood that variations and modifications can be effected within the spirit and scope of the invention.

PARTS LIST**[0019]**

10	microwave oven noise energy waveform	5
11	quiescent (off) period	
12	transceiver	
14	transmitter	
16	receiver	
17	Receive Signal Strength Indicator (RSSI)	10
18	transmit/receive switch	
20	antenna	
22	microwave noise detect circuit	
24	microprocessor	
26	imaging appliance	15
28	Lock detect signal	
30	Sync clock signal	
32	Sync clock circuitry	
34	binary counter	
36	binary counter	20
38	pre-setable counter	
40	pre-setable counter	
42	monostable multivibrator	
44	Sync Stop signal	
46	buffer	25
48	phase comparator	
50	low pass loop filter	
52	Voltage Controlled Oscillator	
54	phase error signal	
56	phase locked loop	

Claims

1. A wireless communication system operating in the 2.4 to 2.5 GHz band in the presence of noise from a microwave oven, the communication system including a transmitter, comprising:
 - a) means for detecting quiescent periods in the microwave oven noise; and
 - b) means for controlling the transmitter to transmit only during the quiescent periods.
2. The wireless communication system claimed in claim 1, wherein the means for detecting quiescent periods in the microwave noise further comprises:
 - a) means for detecting the amplitude of the microwave oven energy; and
 - b) means for recognizing the AC line frequency characteristics of microwave oven energy from the detected amplitude.
3. The wireless communication system claimed in claim 2 further comprising:
 - a) means for generating a signal which represents the microwave oven quiescent period

signature,

b) means for continuing to generate the signal even when the microwave oven noise is not present, and
 c) and means responsive to the signal for enabling the transmitter to transmit when it predicts the oven is in a quiescent state, thereby making the transmission process efficient.

- 10 4. A method of operating a wireless communication system in the 2.4 to 2.5 GHz band in the presence of noise from a microwave oven, the communication system including a transmitter, comprising the steps of:
 - a) detecting quiescent periods in the microwave oven noise; and
 - b) controlling the transmitter to transmit only during the quiescent periods.
- 15 5. The method claimed in claim 4, further comprising the steps of:
 - a) detecting the amplitude of the microwave oven energy; and
 - b) recognizing the AC line frequency characteristics of microwave oven energy from the detected amplitude, using a phase lock loop.
- 20 30 6. The method claimed in claim 5 further comprising the steps of:
 - a) generating a signal which represents the microwave oven quiescent period signature,
 - b) continuing to generate the signal even when the microwave oven noise is not present, and
 - c) employing the generated signal to enable the transmitter to transmit when it predicts the oven is in a quiescent state, thereby making the transmission process efficient.

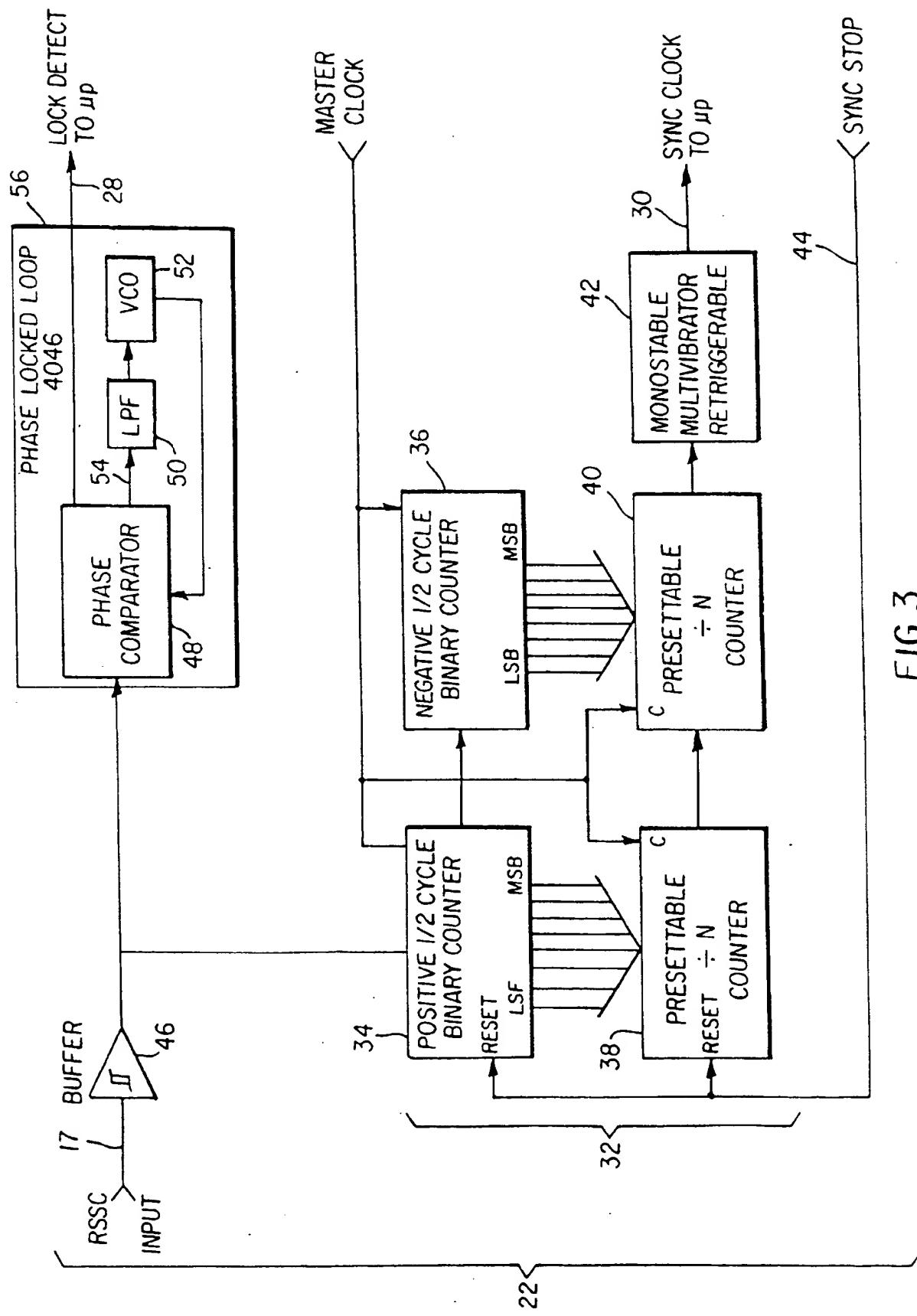
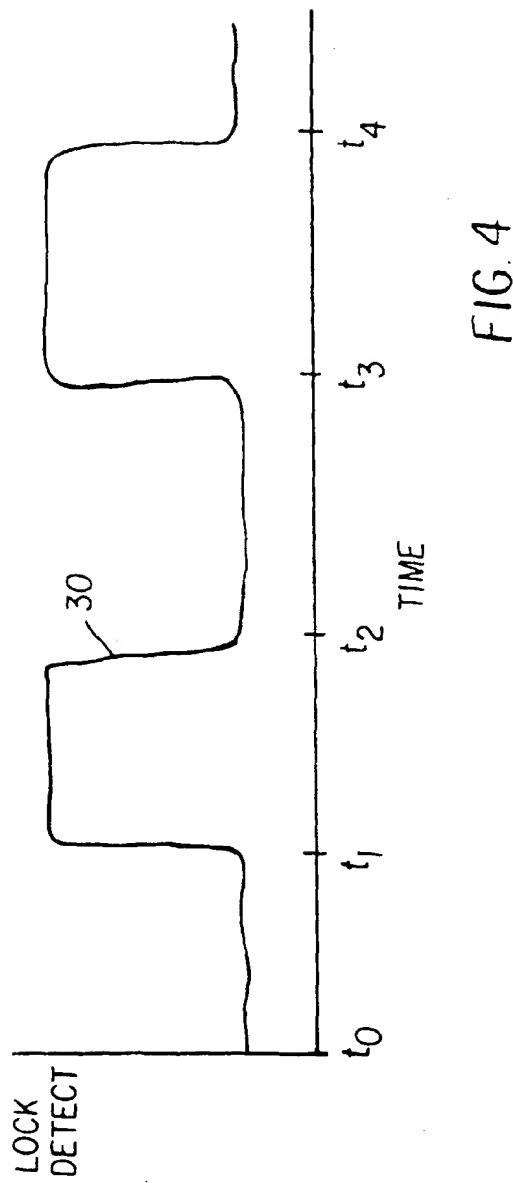
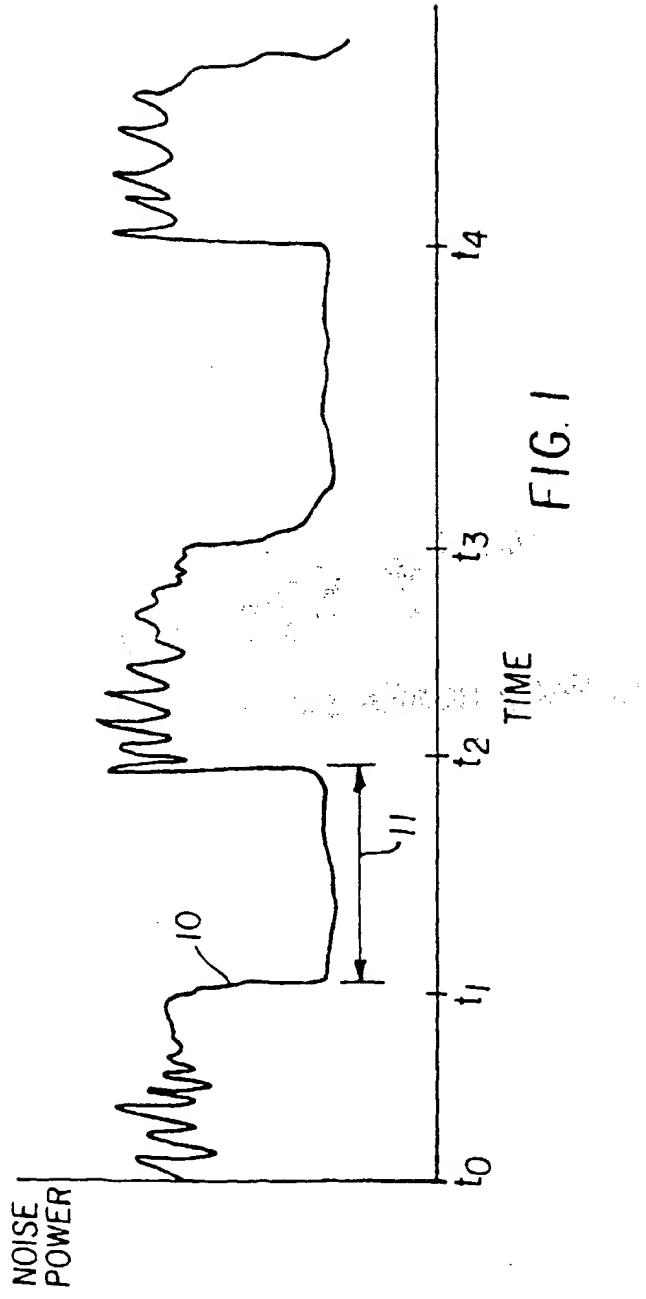


FIG. 3

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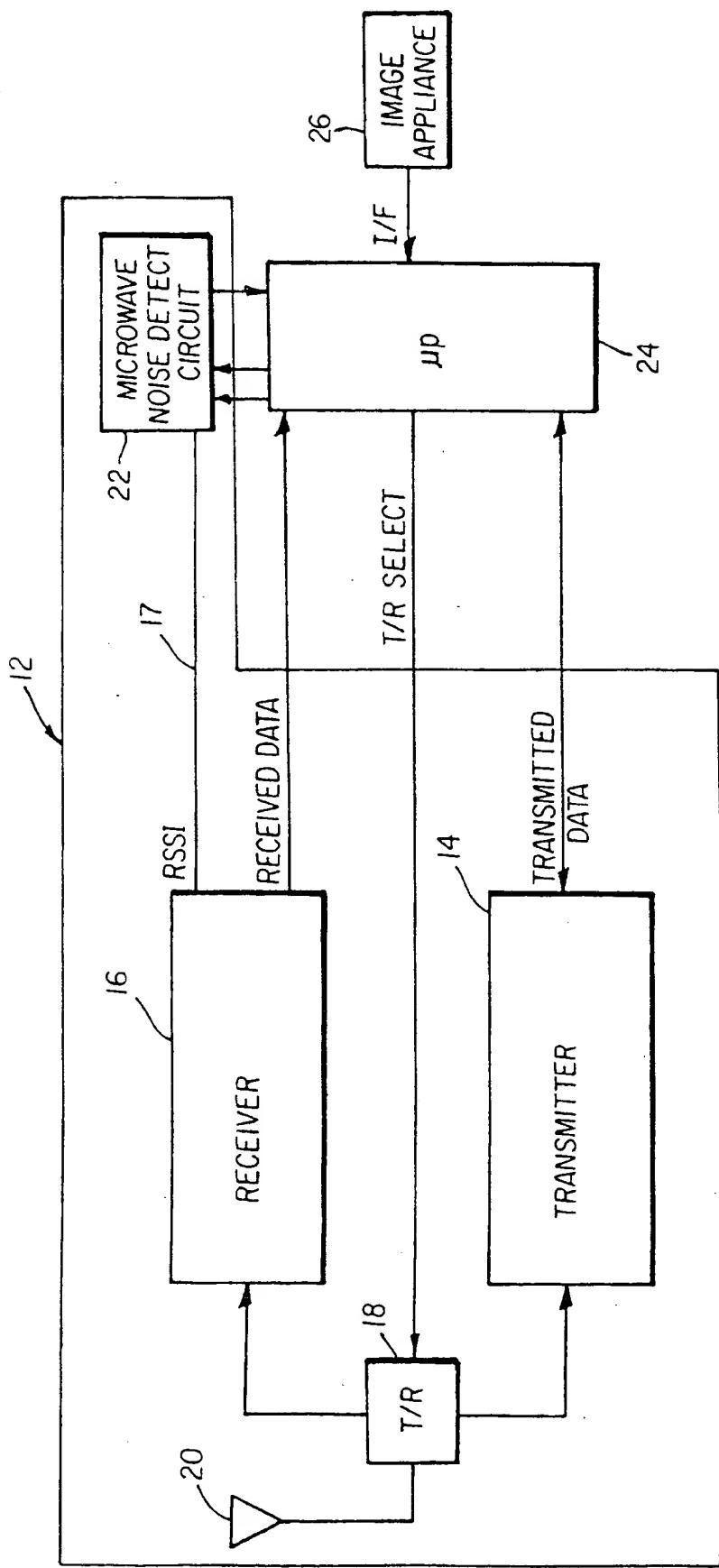


FIG. 2